

Docket No.: 49657-217

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277  
Shigeki OHBAYASHI : Confirmation Number: 8805  
Application No.: 09/196,136 : Group Art Unit: 2133  
Filed: November 20, 1998 : Examiner: Lamarre, Guy J.

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING BONDING OPTIONAL  
FUNCTION

**RESPONSE TO RESTRICTION REQUIREMENT**

Mail Stop Restriction Requirement  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Noting the Office Action of January 6, 2005 wherein restriction has been required,  
Applicant hereby elects Group I (claims 1 through 11) for prosecution in the above-identified  
application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is  
hereby made. Please charge any shortage in fees due in connection with the filing of this paper,  
including extension of time fees, to Deposit Account 500417 and please credit any excess fees to  
such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP



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Date: February 2, 2005

**Please recognize our Customer No. 20277  
as our correspondence address.**